

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Patent No.:	7,367,016	§	Examiner:	Romano, John J.
Issued:	April 29, 2008	§	Group/Art Unit:	2192
Application No.:	10/618,829	§	Atty. Dkt. No:	5681-41100
Filed:	July 14, 2003	§	Conf. No.	5944
Inventor(s):		§		
Meyer et al.		§		
		§		
		§		
		§		
Title:	Method and System for	§		
	Expressing the Algorithms	§		
	for the Manipulation of	§		
	Hardware State Using an	§		
	Abstract Language	§		
		§		
		§		
		§		

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**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Transmitted herewith for filing in the captioned case is the following:

Form PTO-1050. Errors which occur at important points in the captioned patent or which may otherwise affect the understanding or interpretation of the patent are thereon corrected.

All of the errors shown in PTO-1050 are due to Patent Office oversights. A Certificate of Correction is requested under 35 U.S.C. §254. Applicants believe that no fees are required, however, should any fees be required, please deduct them from Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505\5681-41100/SJC.

Respectfully submitted,

/ B. Noël Kivlin /

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